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Class	Subclass
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PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

SCANNED

Q.A.

APPLICATION NO. 09/817703	CONT/PRIOR	CLASS 257 S10	SUBCLASS 30	ART UNIT 23 38	EXAMINER F. J. J. J.
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APPLICANTS

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Bao-Sung Yeh
S. Wang
Cathy Peltier

TITLE

LDMOS and CMOS integrated circuit and method of making

PTO-2040
12/99

ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) _____ (Date)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ _____ (Primary Examiner) _____ (Date)				
				ISSUE FEE	
				Amount Due:	Date Paid
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